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	Filing Date		2003-10-09	
	First Named Inventor	Chandan Mathur		
	Art Unit	2183		
	Examiner Name	David J. Huisman		
Attorney Docket Number		1934-012-03		

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	1	Final Office Action for US Patent Application No. 10/684057, mailed 2006-04-19	<input type="checkbox"/>
	2	IEEE VHDL MATH PACKAGE STUDY GROUP: "Package_Math Real Version 0.7 9header only)" 28 May 1993, XP002372288 URL <a href="http://tams-www.informatik.uni-hamburg.de/vhdl/packages/P1076.2/mathpack.vhd">http://tams-www.informatik.uni-hamburg.de/vhdl/packages/P1076.2/mathpack.vhd</a>	<input type="checkbox"/>
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